

# Socket for Xilinx VIRTEX7 / KINTEX Altera Stratix10

**New released IC Test Socket for  
GTX/GTY transceiver 12.5Gbps & 32Gbps**



## Features

- Evaluation & Verification for high-density and high-speed FPGA
- Cooling set will be designed by calculating heat spread.
- Solder-less mount.
- Best solution for prototyping use.
- Reuse expensive FPGA's

